



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Claude R. Gauthier, Pradeep R. Trivedi, Raymond A. Heald, Gin S. Yee  
Assignee: Sun Microsystems, Inc.  
Title: Compensation Technique to Mitigate Aging Effects in Integrated Circuit Components  
Serial No.: 10/771,989 Filed: February 4, 2004  
Examiner: Arnold M. Kinhead Group Art Unit: 2817  
Docket No.: P9427 Customer No.: 33438

Austin, Texas  
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COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

*Alt 6/25/06*

**RESPONSE TO FINAL OFFICE ACTION**

Sir:

This paper is responsive to the Office Action dated January 24, 2006, having a shortened statutory period expiring on April 24, 2006. Also enclosed is a Petition for Extension of Time along with the fee for the requested extension. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.